



APPENDIX A
VERSION OF SPECIFICATION AND CLAIMS
WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS

Claims 25-30 are newly added, so no marked-up version is shown for these claims.

17. (Once Amended) An electronic device comprising:
- a receiver to detect a first signal from a first device operating at a first hopping frequency;
 - a processor to determine a time frame for a contention-free period;
 - a transmitter to send a second signal to a second device operating at a second hopping frequency [in response to detecting the first signal], the second signal to indicate the time frame for the contention-free period, the transmitter to further communicate with the first device at the first hopping frequency during the contention-free period.
22. (Once Amended) A computer readable medium having stored thereon a set of instructions that, when executed by a computer, cause the computer to:
- receive a first signal from a first device operating at a first hopping frequency;
 - determine a time frame for a contention-free period;
 - send a second signal to a second device operating at a second hopping frequency [in response to receiving the first signal], the second signal indicating the time frame for the contention-free period; and
 - communicate with the first device at the first hopping frequency during the contention-free period.